



Docket No.: 67471-023

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277  
:   
TAKAYAMA, SHUICHI, et al. : Confirmation Number: 5392  
:   
Application No.: 09/301,284 : Group Art Unit: 2122  
: Allowed: December 02, 2004  
Filed: April 28, 1999 : Examiner: Ted T. Vo  
:

For: PROCESSOR FOR EXECUTING INSTRUCTIONS IN UNITS THAT ARE  
UNRELATED TO THE UNITS IN WHICH INSTRUCTIONS ARE READ, AND  
A COMPILER, AN OPTIMIZATION APPARATUS, AN ASSEMBLER, A  
LINKER, A DEBUGGER AND A DISASSEMBLER FOR SUCH PROCESSOR

**LETTER SUBMITTING FORMAL DRAWINGS**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability dated December 02, 2004, submitted  
herewith are Forty (40) sheets of Formal Drawings in connection with the above  
referenced application.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

  
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**Date: February 28, 2005**

**Please recognize our Customer No. 20277  
as our correspondence address.**